



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. ....10/618,471  
Confirmation No. ....5311  
Filing Date ..... July 11, 2003  
Inventor ..... Luan C. Tran  
Assignee ..... Micron Technology, Inc.  
Group Art Unit.....2813  
Examiner ..... Laura M. Schillinger  
Attorney's Docket No. ....MI22-2331  
Customer No.....021567  
Title: Semiconductor Processing Methods of Forming Integrated Circuitry

**RESPONSE TO JUNE 28, 2005 FINAL OFFICE ACTION**  
**ACCOMPANYING REQUEST FOR CONTINUED EXAMINATION (RCE)**

To: Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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Responsive to the Office Action dated June 28, 2005, Applicant amends and  
remarks as follows.

**AMENDMENT**

Underlines indicate insertions and ~~strikeouts~~ indicate deletions.